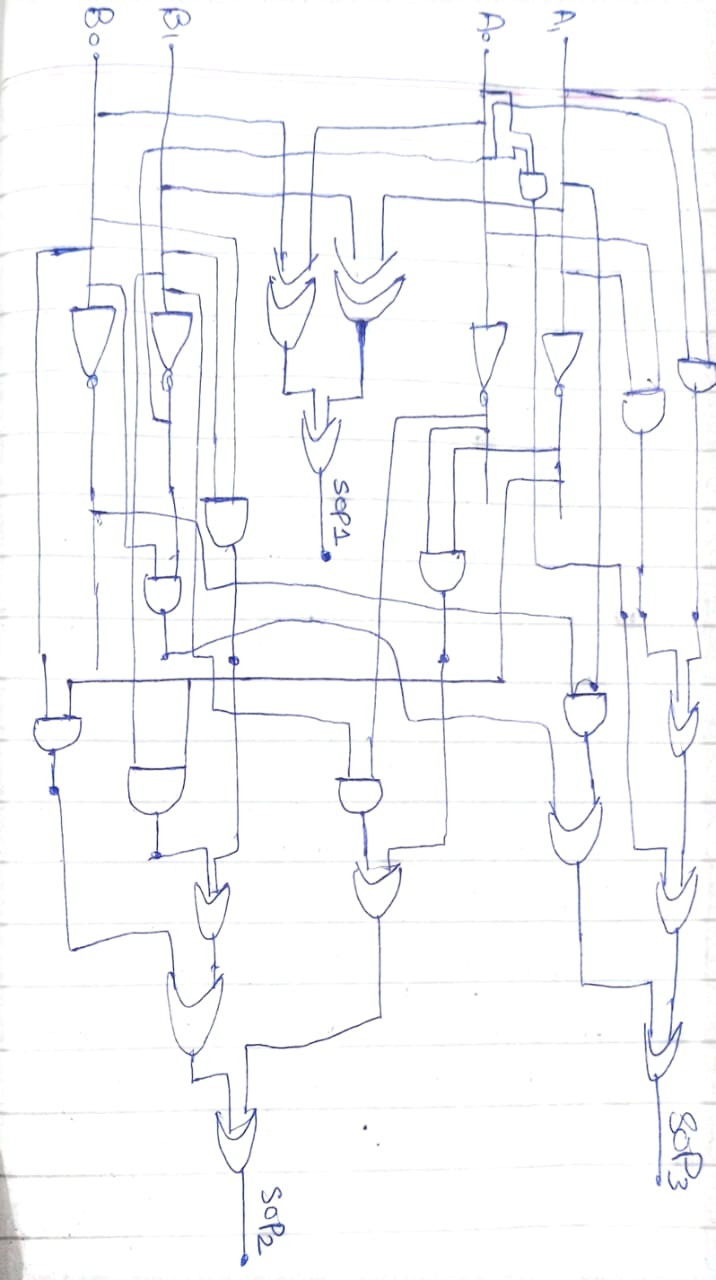
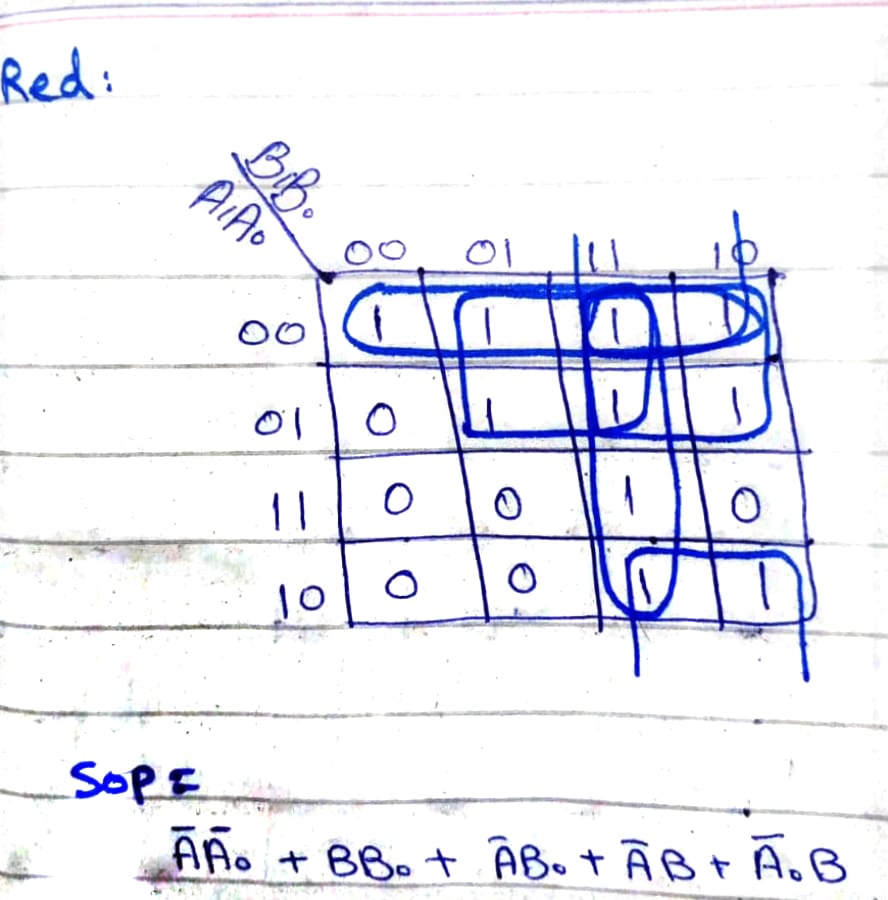
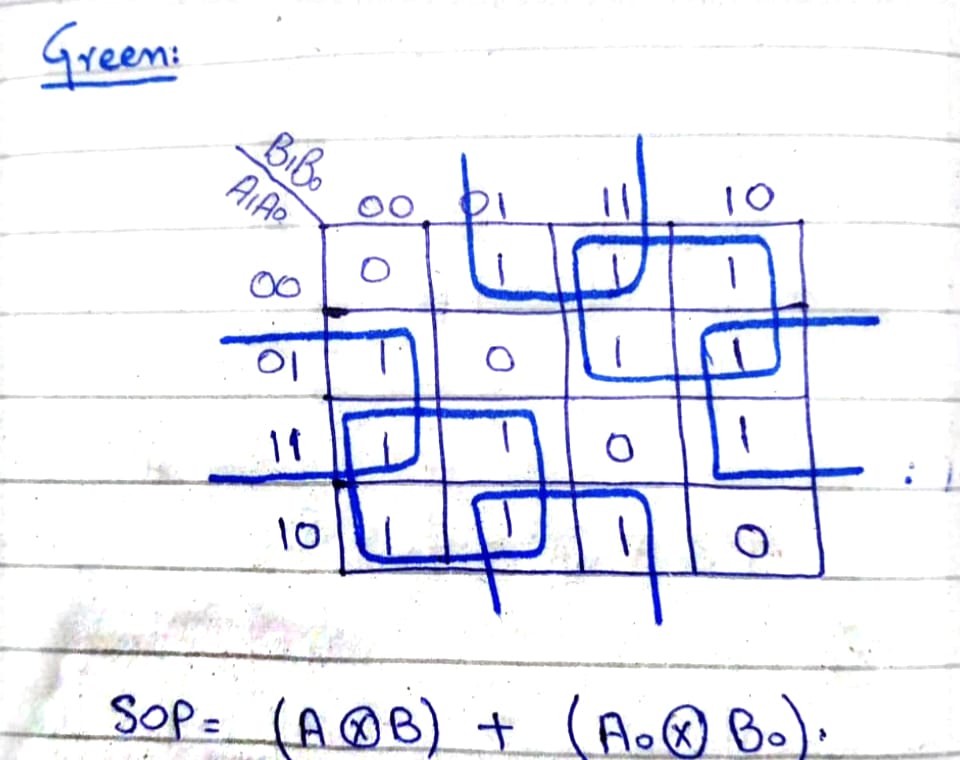
Lab 4

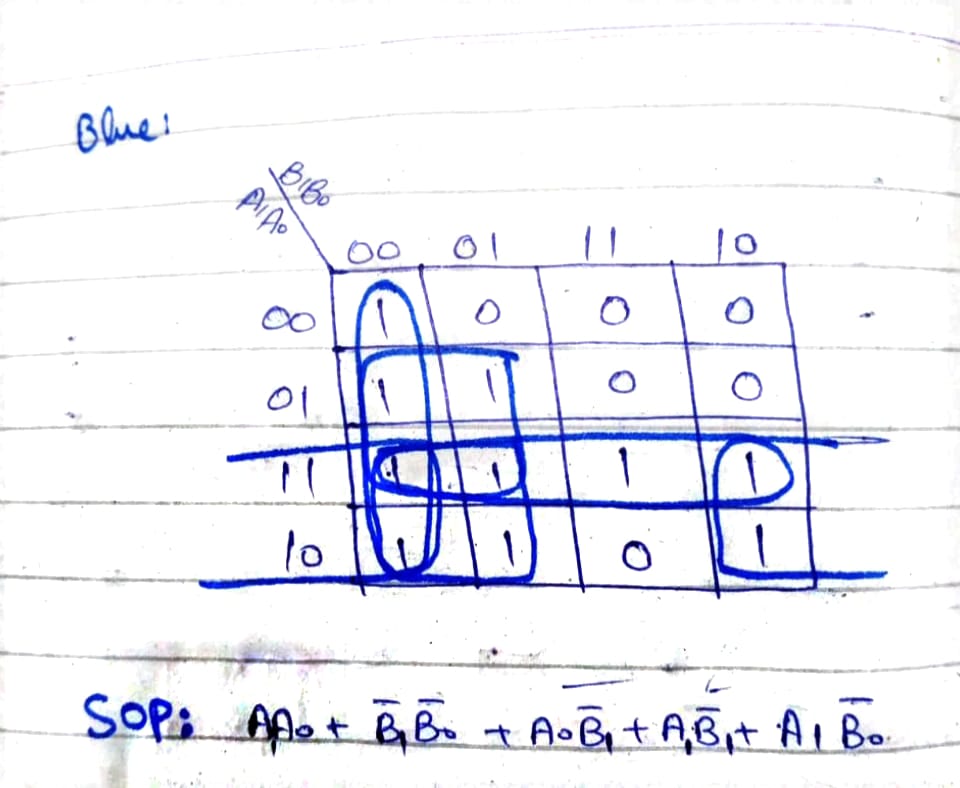
Report

Circuit diagram:

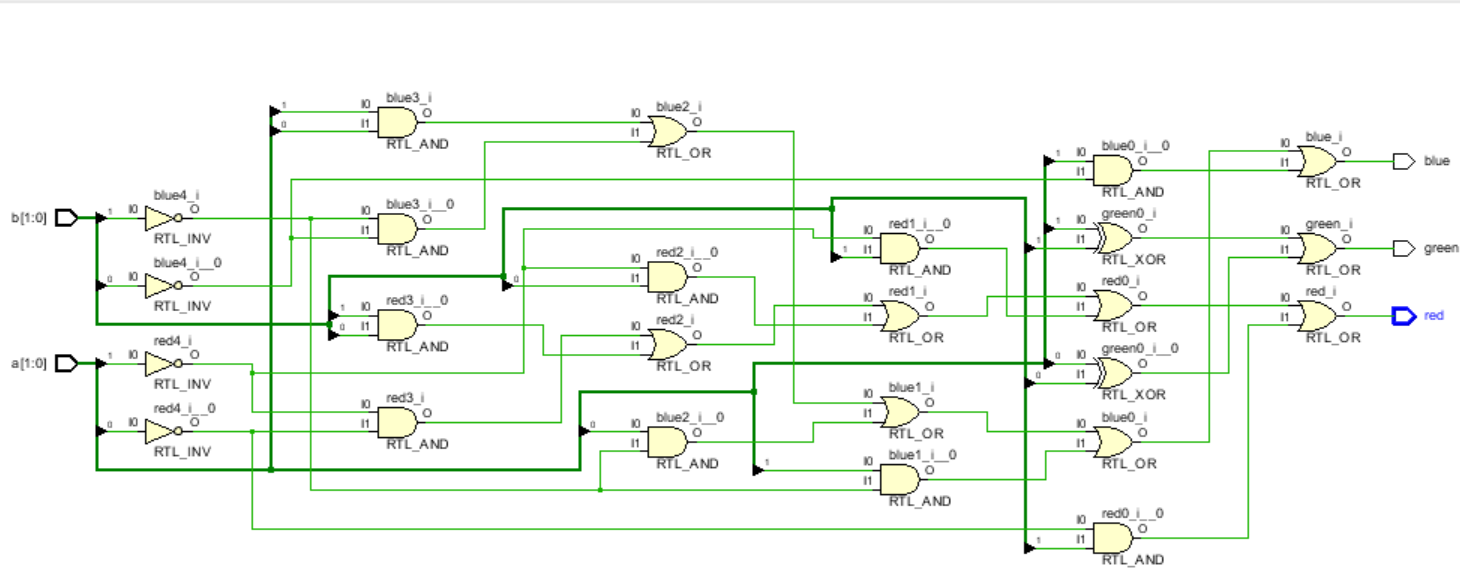


K-Maps diagram:

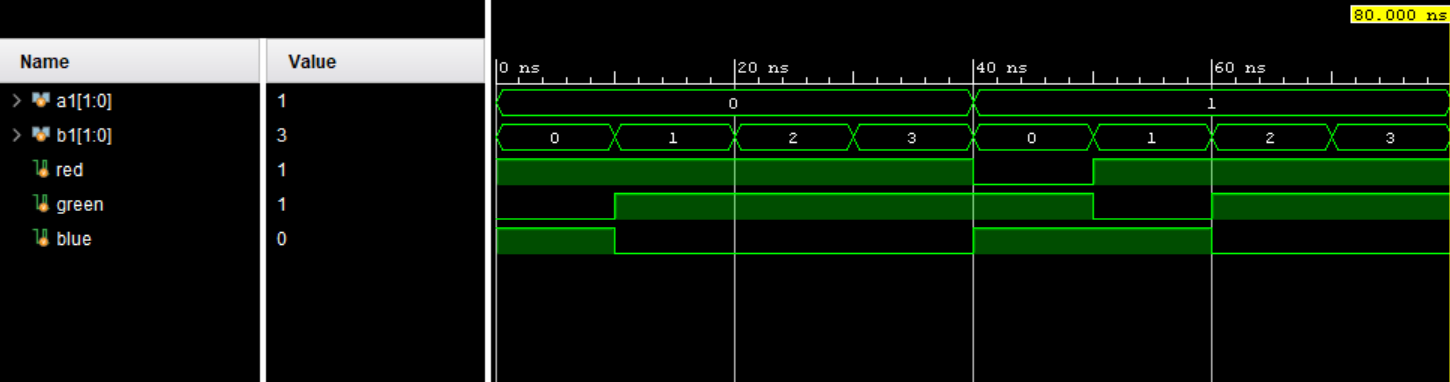




Schematic Diagram:

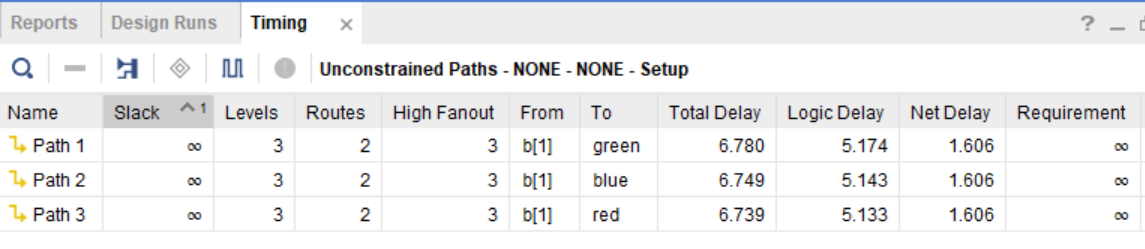


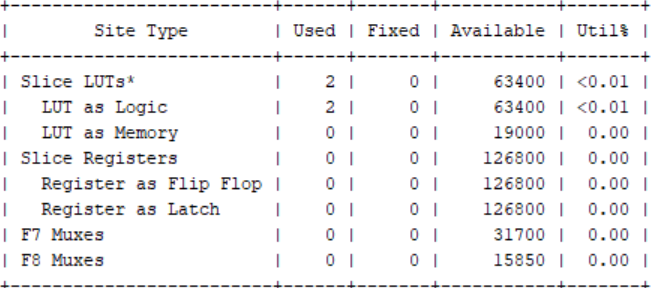
Graph Diagram:

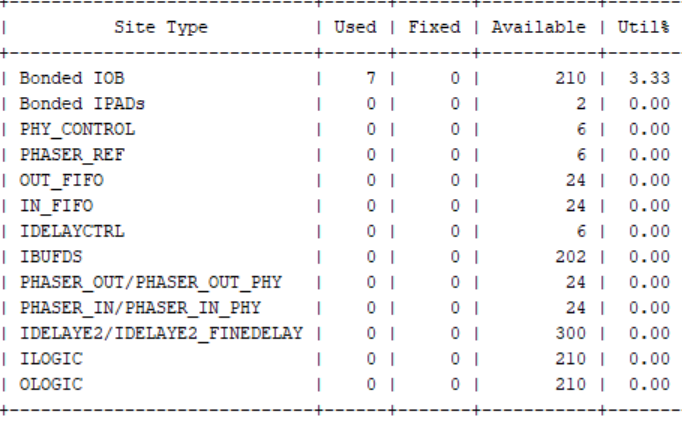


**Reports:**

Time delay:



Table of Contents:



Truth table:

